Docket No. 247078US25CONT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Daniel LITAIZE, et al.

SERIAL NO: New Application

pplication GAU:

FILED: Herewith EXAMINER: Not Assigned

FOR: PROCESS FOR EXCHANGING INFORMATION IN A MULTIPROCESSOR SYSTEM

## INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

### REFERENCES

The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed
references are attached, where required, as are either statements of relevancy or any readily available English
translations of pertinent portions of any non-English language references.

☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

#### RELATED CASES

Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present
application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s)
is attached along with PTO 1449.

☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

# CERTIFICATION

Each item of information contained in this information disclosure statement was first cited in any communication
from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of
this statement

□ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

# DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Not Assigned

Charles L. Gholz

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W. Todd Baker

Registration No. 45,265

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	DB	5	0	8	3	2	9	6	1/21/92	Hara et al.	<del>                                     </del>		IF AFFINE DIST	
	DC	5	0	7	7	6	9	3	12/31/91	Hardee et al.				
-	DD	5	0	1	8	1	1	1	5/21/91	Madland		-		
	DE	5	0	1	6	2	2	6	5/14/91	Hiwada et al.				
	DF	4	9	7	5	8	7	2	12/4/90	Zaiki				
	DG	4	9	7	0	4	1	8	11/13/90	Masterson				
	DH	4	9	5	4	9	8	7	9/4/90	Auvinen et al.			· · · · · · · · · · · · · · · · · · ·	
_	IDI	4	9	5	3	1	2	8	8/28/90	Kawai et al.				
	Ŋ	4	9	5	1	2	5	1	8/21/90	Yamaguchi et al.				
	DK	4	8.	3	7	7	3	4	6/26/90	Bechtolsheim			<del></del>	
	СК	S. N	/luchm	<del></del>			<u> </u>			on Multibus IP, New Electronics		6, p. 31-32,	Aug. 11, 1987	
!	CL	T.C.	. Poon	et al.,	, "A CN	MOS D	RAM-	Contro	oller Chip imp	plementation", IEEE Journal of	Solid State Circ	uits. Vol. 22	No 3	
					n. 1987							uito, 7	, 140. U,	
!	СМ	к. о	)hta, */	A 1-Mt	olt DR/	AM with	h 33-N	/Hz Se	erial I/O Ports	s", IEEE Journal of Solid State (	Circuits, Vol. 21	No. 5, pp.	649-654	
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	co	"Moti	orola 6	38030	Cache	Organ	nizatio	n", por	sting to Interr	net Newsgroup net.arch by agle		CP,		
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	СР	Raym	nond F	inkha	m et a	l., "A F	ligh S	peed [	Jual Port Mer	mory with Simultaneous Serial	and Random Mo	ode Access	for Video	
										. SC-19, No. 6, pp. 999-1007, D				
										usible Links*, IEEE Journal of S		uits, Vol. 18,	No. 5.	
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	DL	4	8	2	8	2	6	5	5/22/90	Higuchi et al.			No.
	DM	4	8	2	0	4	8	3	4/24/90	Pogue et al.		<u> </u>	
	DN	4	9	1	6	6	7	0	4/10/90	Suzuki et al.			
	DO	4	8	1	2	6	3	0	3/27/90	Cochcroft, Jr.			
	DP	4	8	8	2	7	1	2	11/21/89	Ohno et al.			
	DQ	4	8	4	5	6	6	4	7/4/89	Alchelmann, Jr. et al.			
	DR	4	8	2	1	2	2	6	4/11/89	Christopher et al.			
	DS	4	8	0	7	1	8	9	2/21/89	Pinkham et al.			
	DT	4	7	9	9	1	9	9	1/17/89	Scales, III et al.			
	DU	4	7	8	8	6	6	7	11/29/88	Nakano et al.			
,				01	THER !	ART (I	NCLU	DING	AUTHOR, TI	ITLE, DATE, PERTINENT PA	GES, ETC.)		
-	CR	M. /	Bazes	et al.,	, "A Pro	ogramı	mable	NMOS	S DRAM Con	troller for Microcomputer System	tems with Dual-F	Port Memor	y and Error Checking
	<u>'</u>									Vol. 18, No. 2, pp. 164-172, A			
	cs									tching Memory and I/O Channe	<del></del>	cal Disclosu	ure Bulletin, Vol. 24,
										ingle sheet), Mar. 1982			
	ст	R.W	v. Call	ahen e	et al., "	Burst-	Mode	PIO B	us Control*, If	BM Technical Disclosure Bulle	etin, Vol. 21, No	. 4, pp. 141	7-1419,
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		pp. ?	33-39,	, Feb.	1976								
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	DV	4	7	3	4	8	8	0	3/29/88	Collins				
	DW	4	6	8	0	7	3	8	7/14/87	Tam				
	DX	4	6	7	5	8	5	0	6/23/87	Kumanoya et al.				
	DY	4	5	7	0	2	2	0	2/11/86	Tetrick et al.				
	DZ	4	5	1	9	0	3	4	5/21/85	Smith et al.			<del></del>	
	EA	4	4	4	5	2	0	4	4/24/84	Nishiguchi				
	EB	4	3	1	5	3	0	8	2/9/82	Jackson				
	EC	4	2	4	7	8	1	7	1/27/81	Heller				
	ED	4	1	8	3	0	9	5	1/8/90	Ward			<del></del>	
	EE	4	0	8	2	6	6	5	5/30/78	Saran				
	EF	4	0	8	4	1	5	4	4/11/78	Panigraphi			· · · · · · · · · · · · · · · · · · ·	
	EG	3	8	2	1	7	1	5	6/28/74	Hoff, Jr. et al.				
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